

Abstract

A coupler circuit for sampling an output power of a signal from an output power source has at least one first sampling element for sampling a first portion of the signal and at least one second sampling element for sampling a second portion of the signal. The first sampling element and the second sampling element are separated by an output matching network defined by a set of S-parameters. A processor coupled to the at least one first and second sampling elements determines the output power based on at least the first portion of the signal and the second portion of the signal. A detector may be coupled to the processor to measure whether the first and second portion of the signal or the output power determined by the processor.